Curriculum for M Tech 3-Years Program in Electrical Engineering Starting from January (Power Electronics & Power Systems)

	Semester I			Semester II	
EE5240	Electrical Machine			Steady State Power	
	Analysis and Control	3	EE5200	System Analysis	3
	Core Elective 1			Power Converter	
		3	EE5210	Design	3
			EE5201	Power System Lab	2
			EE5301	DSP Lab	2
			EE5215	Thesis (Stage 0)	1
				Total Semester	10
	Total Semester Credits	6		Credits	+1
	Semester III			Semester IV	
EE5230	Power System			Advanced Control	
	Dynamics and Control	3	EE5220	System	3
				Power Electronics and	
EE6235	Thesis (Stage 1)	2	EE5211	Machines Lab	2
	Core Elective 2	3		Core Elective 4	3
	Core Elective 3	3	EE6245	Thesis (Stage II)	2
				Total Semester	
	Total Semester Credits	11		Credits	10
				Semester VI	
	Semester V			Thesis (Stage IV)	12
_				Total Semester	
EE6255	Thesis (Stage III)	10	EE6265	Credits	12
	Total Semester Credits	10			
	Total Credits	60			

Curriculum for a 3-year M Tech program Starting from January (Communication and Signal Processing)

	Semester I			Semester II	
	Communication		EE5321/	Communications Lab	
EE5340	Network & Systems	3	EE5301	or DSP lab	2
				Probability and	
			EE5310	Random Processes	3
				Digital Signal	
	Core Elective 1	3	EE5300	Processing	3
				Digital	
			EE5320	Communications	3
			EE5315	Thesis (Stage 0)	1
	Total Credits	6		Total Credits	11
	Semester III			Semester IV	
	Information Theory and				
EE5330	Coding	3		Core Elective 3	3
			EE5321/		
	Core Elective 2	3	EE5301	Communications lab	2
EE6000	Self Study	2		0 5 4	
EE6335	Thesis (Stage I)	2		Core Elective 4	3
			EE6345	Thesis (Stage II)	2
	Total Credits	10		Total Credits	10
	Semester V			Semester VI	
EE6355	Thesis (Stage III)	10	EE6365	Thesis (Stage IV)	12
	, ,			, ,	
	Total Credits	10		Total Credits	12
	Overall credits	60			

Curriculum for a 3-year M Tech program from January (Microelectronics and VLSI)

Analog IC Design	3	EE5110	Semiconductor Devices	
	3	FF5110		
	3	FE5110	0 84 1 111	
O EL 0 4		LLJIIU	& Modelling	3
Core Elective 1	3	EE5120	VLSI Technology	4
VLSI Design Lab	2	EE5111	Device Simulation Lab	2
		EE5300	Digital Signal Processing	3
		EE5115	Thesis (Stage 0)	1
Total Credits	8		Total Credits	13
Semester III			Semester IV	
			Jeniester IV	
	3			
Vormoution		EE5301	DSP Lab	2
Core Elective 2	3			3
Microelectronics Lab		EE6145	<u> </u>	2
Thesis (Stage I)	2		,	
Total Credits	10		Total Credits	7
Semester V			Semester VI	
Thesis (Stage III)	10	EE6165	Thesis (Stage IV)	12
Total Credits	10		Total Credits	12
Overall credits	60			
	Semester III Digital IC Design and Verification Core Elective 2 Microelectronics Lab Thesis (Stage I) Total Credits Semester V Thesis (Stage III)	Total Credits Semester III Digital IC Design and Verification Core Elective 2 Microelectronics Lab Thesis (Stage I) Zotal Credits Semester V Thesis (Stage III) Thesis (Stage III)	EE5300 EE5115 Total Credits	EE5300 Digital Signal Processing