1. Equipment: SUSS PLV 50
2. Indentor: *CASCADE MICROTECH USA*
3. Past /Present users (departments): *EE*
4. Preference of slot types (mention minimum time required for one time use):
5. Hours: *Not applicable as testing requires few days / few hours*
6. Samples: *Minimum die size 1.5mm x1.5mm*

(For example: TEM has one slot of 3 hours with user fee of 1,500 Rs.)

1. Target groups: *VLSI Chip Design*
2. Number of minimum students need to be trained: *Not applicable. As Already allotted a Technician and PhD student to handle this instruments due to high cost of accessories*
3. How would you like to train the students/staffs?

(Please mention the approximate training duration too)

*As Already allotted a Technician and PhD student to handle this instruments. Students are taking training from them*

1. Samples/materials allowed in the equipment: *Si (equivalent materials) DIE* , *Minimum die size 1.5mm x1.5mm*
2. Restricted materials/samples in the equipment: Not Applicable
3. Tentative cost per slot/sample:

(please give a reasonable estimate comparing the instrument costs with other facilities at other IITs/Research centres)

*Please see attached ICSL lab user policy*

1. Rules and guidelines:

*Please see attached ICSL lab user policy and we also have a login form.*