

Department of Electrical Engineering, IIT Hyderabad

The following is the list of candidates shortlisted for interview for **M.Tech RA** Admissions under the **CSPL** specialisation at the EE Department of IITH. The interviews will happen in the following order.

Venue: EE Department Block – 2 nd floor - Room EE208

Interview Date and Time - 09-07-2025 3:15 pm to 6:00 pm & 10 July 2025

Application No.	Time	Date
158235	3:30-4:15PM	9th July
146474	3:30-4:15PM	9th July
157450	3:30-4:15 PM	9th July
147125	3:30-4:15 PM	9th July
147237	4:15-5PM	9th July
136353	4:15-5PM	9th July
153855	4:15-5PM	9th July
157674	4:15-5PM	9th July
152261	4:15-5PM	9th July
157083	4:15-5PM	9th July
158037	5:00-6PM	9th July
138507	5:00-6PM	9th July
147198	5:00-6PM	9th July
152896	5:00-6PM	9th July
136391	5:00-6PM	9th July
157816	5:00-6PM	9th July

146948	9-10AM	10th July
148439	9-10AM	10th July
150581	9-10AM	10th July
147523	9-10AM	10th July
140367	9-10AM	10th July
146543	9-10AM	10th July
150988	10-11AM	10th July
152665	10-11AM	10th July
141061	10-11AM	10th July
154407	10-11AM	10th July
144185	10-11AM	10th July
145104	10-11AM	10th July
141893	10-11AM	10th July
151347	11-12AM	10th July
137056	11-12AM	10th July
138792	11-12AM	10th July
154761	11-12AM	10th July
158860	11-12AM	10th July
149572	11-12AM	10th July
157660	11-12AM	10th July