

Department of Electrical Engineering, IIT Hyderabad

The following is the list of candidates shortlisted for interview for **M. Tech (Govt. Lab Sponsored)** Admissions under the **CSPL** at the EE Department of IITH. The interviews will happen in the following order.

Reporting Room EE 002 (Ground Floor)

Electrical Engineering Building

Application No.	Time	Date
158413	2PM - 5PM	10th July