

Department of Electrical Engineering, IIT Hyderabad

The following is the list of candidates shortlisted for interview for **M. Tech SS** Admissions under the **Micro VLSI** at the EE Department of IITH. The interviews will happen in the following order.

Reporting Room EE 002 (Ground Floor)

Electrical Engineering Building

App. Num.	Slot (10th July)
137169	9.30 AM:10:30 AM
140782	
141020	
141353	
142130	
142418	
147222	
150473	10:30 AM-11:30 AM
150644	
155545	
158462	
158622	
136430	
138761	
143795	11:30 AM-12 AM
147044	
148587	
158268	

149418	12-1 PM
148767	
138673	
157916	
158168	2-3 PM
155822	
136507	
148492	
150174	3-4 PM
147502	
142659	
138620	
158109	
153506	
149793	4-6 PM
151771	
157756	
147753	
151390	
149413	
146845	
157026	
145787	