

Department of Electrical Engineering, IIT Hyderabad

The following is the list of candidates shortlisted for interview for **M. Tech RA**

Admissions under the Micro_VLSI (**NDT**) panel at the EE Department of IITH. The interviews will happen in the following order.

Waiting room EE 515 (5th Floor)

App. Num.	Time Slot (9th July)
136353	3:45-4-45 pm
140367	
141061	
141425	
146474	
147125	
147523	
150581	
150988	4:45-6 pm
152062	
152261	
152896	
154179	
157753	
157844	
158037	
158452	