

**M. Tech 3-Yr CSP (Jan.)
(48 Credits)**

| Semester - I | | |
|---------------|----------------|---------|
| C. No. | Course Title | Credits |
| EExxxx | Core Electives | 4 |
| XXxxxx | Free Electives | 3 |
| Total Credits | | 7 |

| Semester - IV | | |
|---------------|-------------------------|---------|
| C. No. | Course Title | Credits |
| EExxx | Core Elective | 2 |
| EE5925 | MTech Thesis Stage - II | 6 |
| Total Credits | | 8 |

| Semester - II | | |
|---------------|--------------------|---------|
| C. No. | Course Title | Credits |
| EE5807 | Advanced DSP | 2 |
| EE5817 | Random Variables | 2 |
| EE5827 | Random Processes | 1 |
| EE5837 | Digital Modulation | 1 |
| EE5801 | CSP Lab | 3 |
| Total Credits | | 9 |

| Semester - V | | |
|---------------|--------------------------|---------|
| C. No. | Course Title | Credits |
| EE5935 | MTech Thesis Stage - III | 8 |
| Total Credits | | 8 |

| Semester - VI | | |
|---------------|-------------------------|---------|
| C. No. | Course Title | Credits |
| EE5945 | MTech Thesis Stage - IV | 8 |
| Total Credits | | 8 |

| Semester - III | | |
|----------------|------------------------|---------|
| C. No. | Course Title | Credits |
| EE5847 | Information Theory | 1 |
| EEXXXX | Core Electives | 3 |
| EE5811 | FPGA Lab | 2 |
| EE5915 | MTech Thesis Stage - I | 2 |
| Total Credits | | 8 |

| Summary | |
|---------------|----|
| Core Theory | 7 |
| Core Lab | 5 |
| Core Elective | 9 |
| Free Elective | 3 |
| Thesis | 24 |